

Abstract

Integrated circuit comprising series-connected subassemblies

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The integrated circuit comprises subassemblies (2) connected in series enabling synchronization of the subassemblies to be achieved in simple manner. Each subassembly comprises a first power supply terminal (B1) and a second power supply terminal (B2) and a clock input (H1 to H5). The subassemblies are connected in series to the terminals of a voltage supply source (3) so that the same current (I) flows through the different subassemblies. The clock input (H1 to H5) of each subassembly (2a to 2e) is connected to a common clock circuit (5) by means of devices (6a to 6e) for shifting the clock signal levels, for example comprising capacitors and/or transistors. Each subassembly can comprise a decoupling capacitor and a voltage limiting circuit between its first and second power supply terminals.

(Figure 1)

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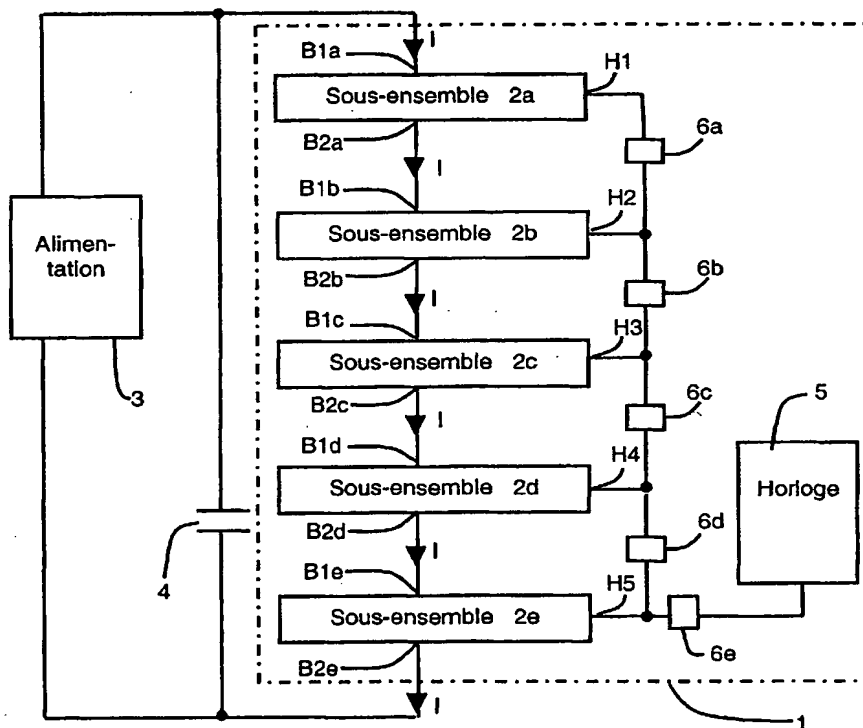
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(54) Title: INTEGRATED CIRCUIT COMPRISING SERIES-CONNECTED SUBASSEMBLIES

(54) Titre : CIRCUIT INTEGRE COMPORTANT DES SOUS-ENSEMBLES CONNECTES EN SERIE



3...POWER SUPPLY
5...CLOCK
2A - 2E...SUBASSEMBLIES

(57) Abstract: The invention concerns an integrated circuit comprising series-connected subassemblies (2), to provide simple synchronization of the subassemblies. Each subassembly includes a first line terminal (B1) and a second line terminal (B2) and a clock input (H1 to H5). The subassemblies are connected in series to the terminals of a supply voltage source (3), such that the various subassemblies circulate the same current (I). The clock input (H1 to H5) of each subassembly (2a to 2e) is connected to a common clock circuit (5) via devices (6a to 6e) adapted to offset the clock signal levels, for example comprising capacitors and/or transistors. Each subassembly can include one by-pass capacitor and one voltage limiting circuit between its first and second line terminals.

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